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COLOR MONITOR

SERVICE MANUAL

CHASSIS NO. : CA-119

FACTORY MODEL: T710BH

MODEL: FLATRON^{ez} T710B (T710BH-EL)
FLATRON^{ez} T710BH (T710BH-HL)
FLATRON^{ez} T710SH (T710BH-SL)

*() ID LABEL Model No.

CAUTION

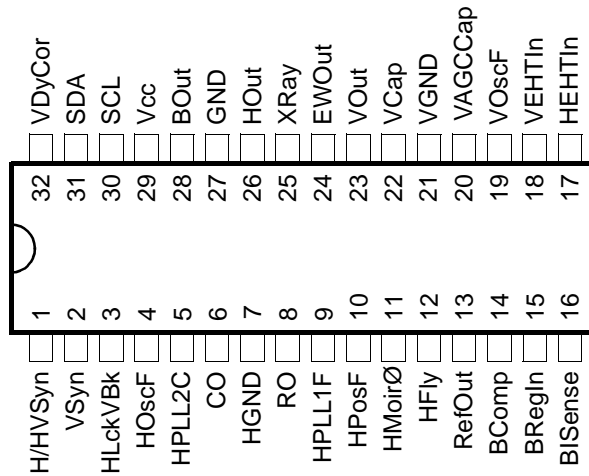
BEFORE SERVICING THE UNIT,
READ THE **SAFETY PRECAUTIONS** IN THIS MANUAL.



PIN CONFIGURATION

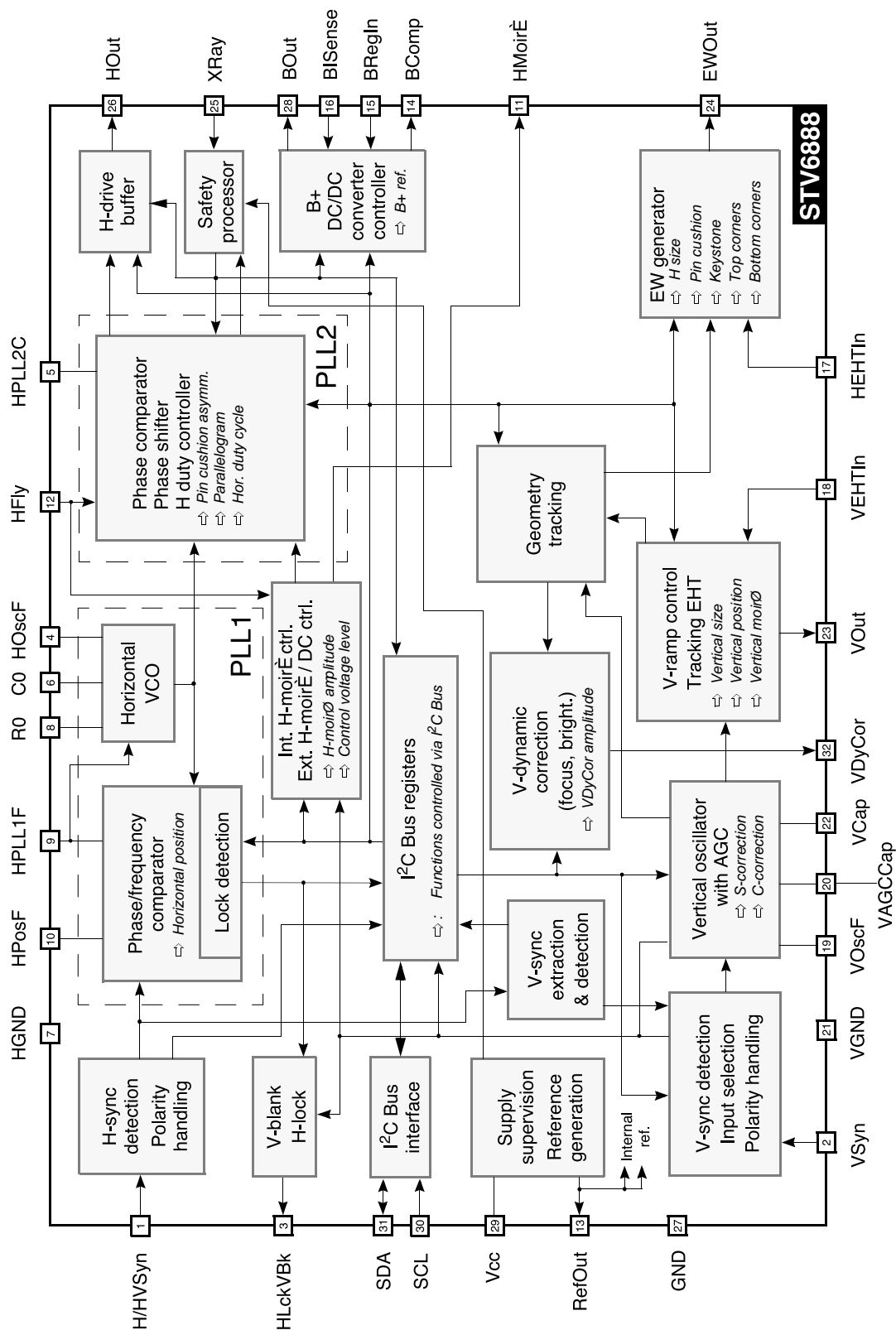
LOW-COST I²C CONTROLLED DEFLECTION PROCESSOR FOR MULTISYNC MONITORS

STV6888



Pin	Name	Function
1	H/HVSyn	TTL compatible H orizontal / H orizontal and V ertical S ync. input
2	VSyn	TTL compatible V ertical S ync. input
3	HLckVBk	H orizontal PLL1 L ock detection and V ertical early B lanking composite output
4	HOscF	High H orizontal O scillator sawtooth threshold level F ilter input
5	HPLL2C	H orizontal P LL2 loop C apacitive filter input
6	CO	Horizontal O scillator C apacitor input
7	HGND	H orizontal section G rou N D
8	RO	Horizontal O scillator R esistor input
9	HPLL1F	H orizontal P LL1 loop F ilter input
10	HPosF	H orizontal P osition F ilter and soft-start time constant capacitor input
11	HMoirE	H orizontal M oirE output
12	HFly	H orizontal F lyback input
13	RefOut	R eference voltage O utput
14	BComp	B + DC/DC error amplifier (C omparator) output
15	BRegIn	R egulation feedback I nput of the B + DC/DC converter controller
16	BISense	B + DC/DC converter current (I) S ense input
17	HEHTIn	I nput for compensation of H orizontal amplitude versus E HT variation
18	VEHTIn	I nput for compensation of V ertical amplitude versus E HT variation
19	VOscF	V ertical O scillator sawtooth low threshold F ilter (capacitor to be connected to VGND)
20	VAGCCap	Input for storage C apacitor for A utomatic G ain C ontrol loop in V ertical oscillator
21	VGND	V ertical section G rou N D
22	VCap	V ertical sawtooth generator C apacitor
23	VOut	V ertical deflection drive O utput for a DC-coupled output stage
24	EWOut	E /W O utput
25	XRay	X - R ay protection input
26	HOut	H orizontal drive O utput
27	GND	Main G rou N D
28	BOut	B + DC/DC converter controller O utput
29	Vcc	Supply voltage
30	SCL	I ² C bus S erial C Lock Input
31	SDA	I ² C bus S erial D Ata input/output
32	VDyCor	V ertical D ynamic C orrection output

Block Diagram



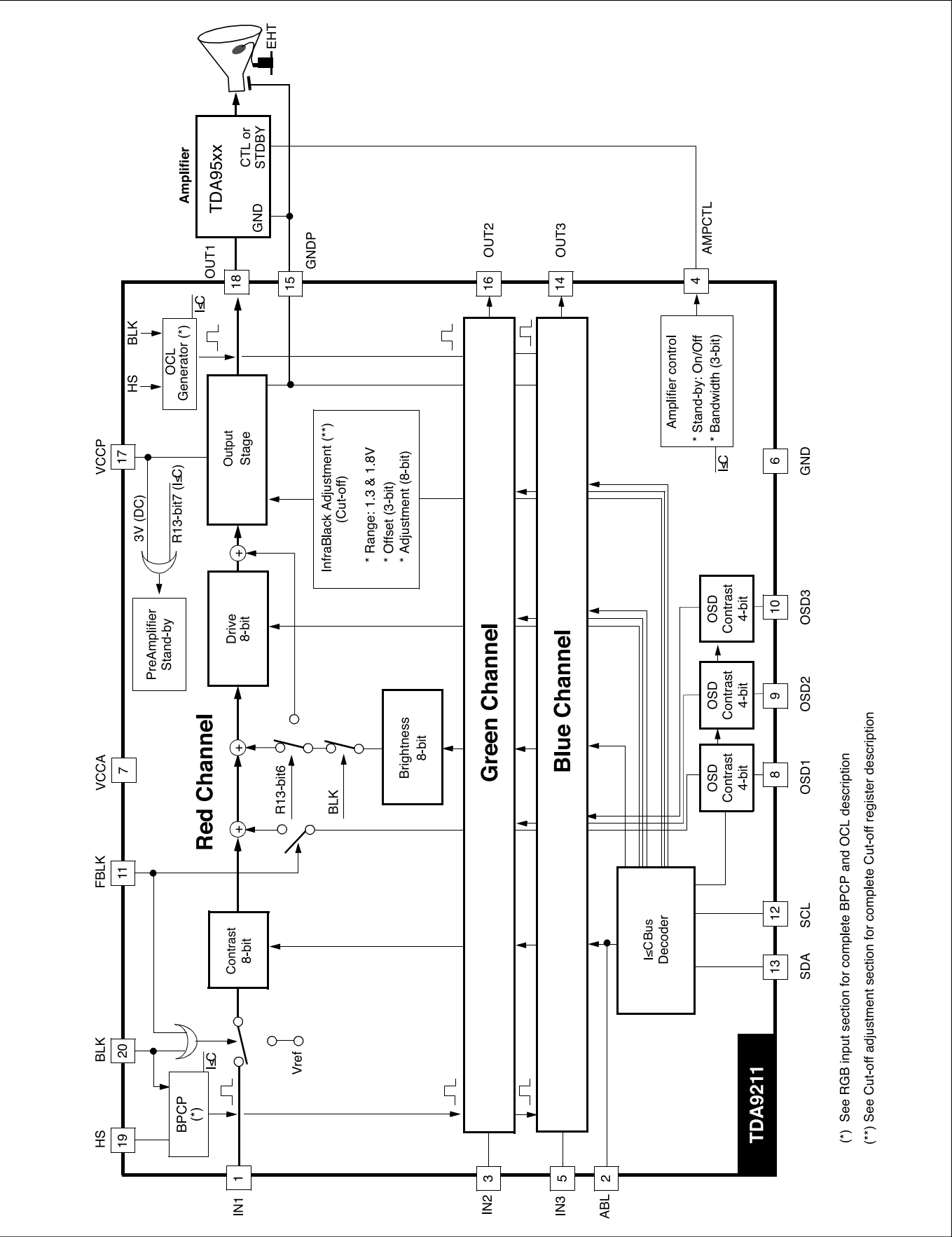
STV9211

Pin Configuration

IN1	<input type="checkbox"/>	1	<input type="checkbox"/>	20	<input type="checkbox"/>	BLK
ABL	<input type="checkbox"/>	2	<input type="checkbox"/>	19	<input type="checkbox"/>	HS
IN2	<input type="checkbox"/>	3	<input type="checkbox"/>	18	<input type="checkbox"/>	OUT1
AMPCTL	<input type="checkbox"/>	4	<input type="checkbox"/>	17	<input type="checkbox"/>	V _{CCP}
IN3	<input type="checkbox"/>	5	<input type="checkbox"/>	16	<input type="checkbox"/>	OUT2
GNDA	<input type="checkbox"/>	6	<input type="checkbox"/>	15	<input type="checkbox"/>	GNDP
V _{CCA}	<input type="checkbox"/>	7	<input type="checkbox"/>	14	<input type="checkbox"/>	OUT3
OSD1	<input type="checkbox"/>	8	<input type="checkbox"/>	13	<input type="checkbox"/>	SDA
OSD2	<input type="checkbox"/>	9	<input type="checkbox"/>	12	<input type="checkbox"/>	SCL
OSD3	<input type="checkbox"/>	10	<input type="checkbox"/>	11	<input type="checkbox"/>	FBLK

Pin Description

Pin number	symbol	description
1	IN1	Video input (channel 1, red)
2	ABL	ABL input
3	IN2	Video input (channel 2, green)
4	AMPCTL	Amplifier control (bandwidth and stand-by). Only applicable with amplifiers with the CTL or STDBY pins. To be connected to ground if not used.
5	IN3	Video input (channel 3, blue)
6	GNDA	Analog ground
7	V _{CCA}	Analog supply (5V)
8	OSD1	OSD input (channel 1, red)
9	OSD2	OSD input (channel 2, green)
10	OSD3	OSD input (channel 3, blue)
11	FBLK	Fast blanking
12	SCL	SCL
13	SDA	SDA
14	OUT3	Video output (channel 3, blue)
15	GNDP	Power ground
16	OUT2	Video output (channel 2, green)
17	V _{CCP}	Output stage supply (5 V to 8 V)
18	OUT1	Video output (channel 1, red)
19	HS	Horizontal synchro or BPCP pulse
20	BLK	Blanking input

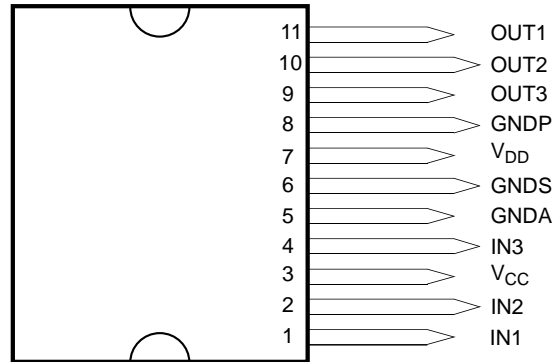


(*) See RGB input section for complete BPCP and OCL description

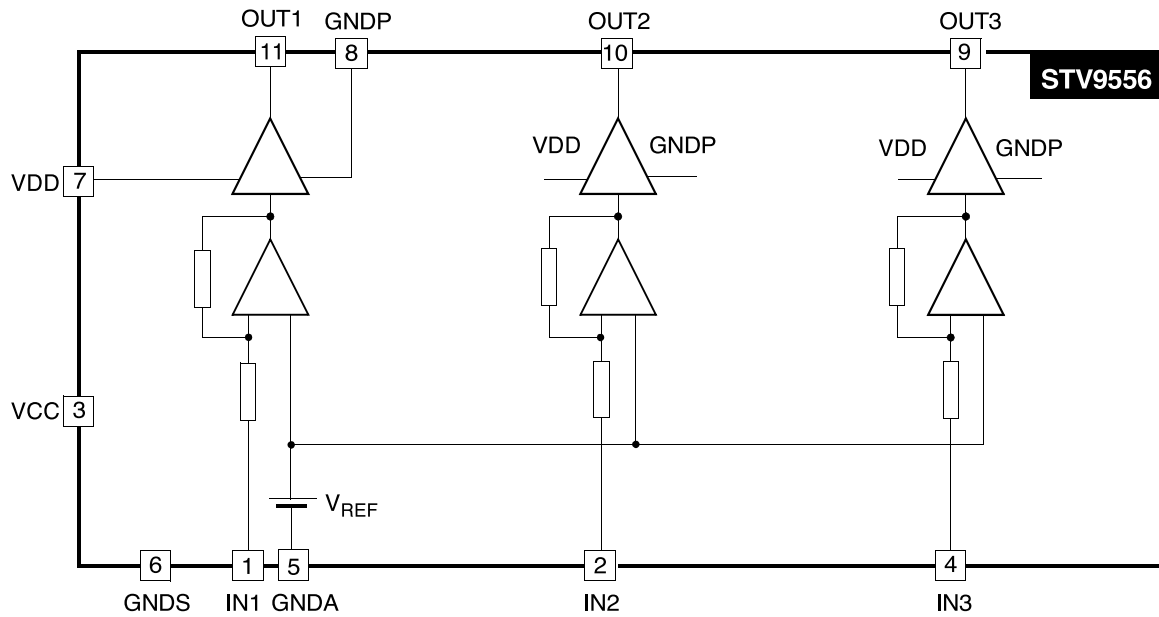
(**) See Cut-off adjustment section for complete Cut-off register description

STV9556

Pin Configuration

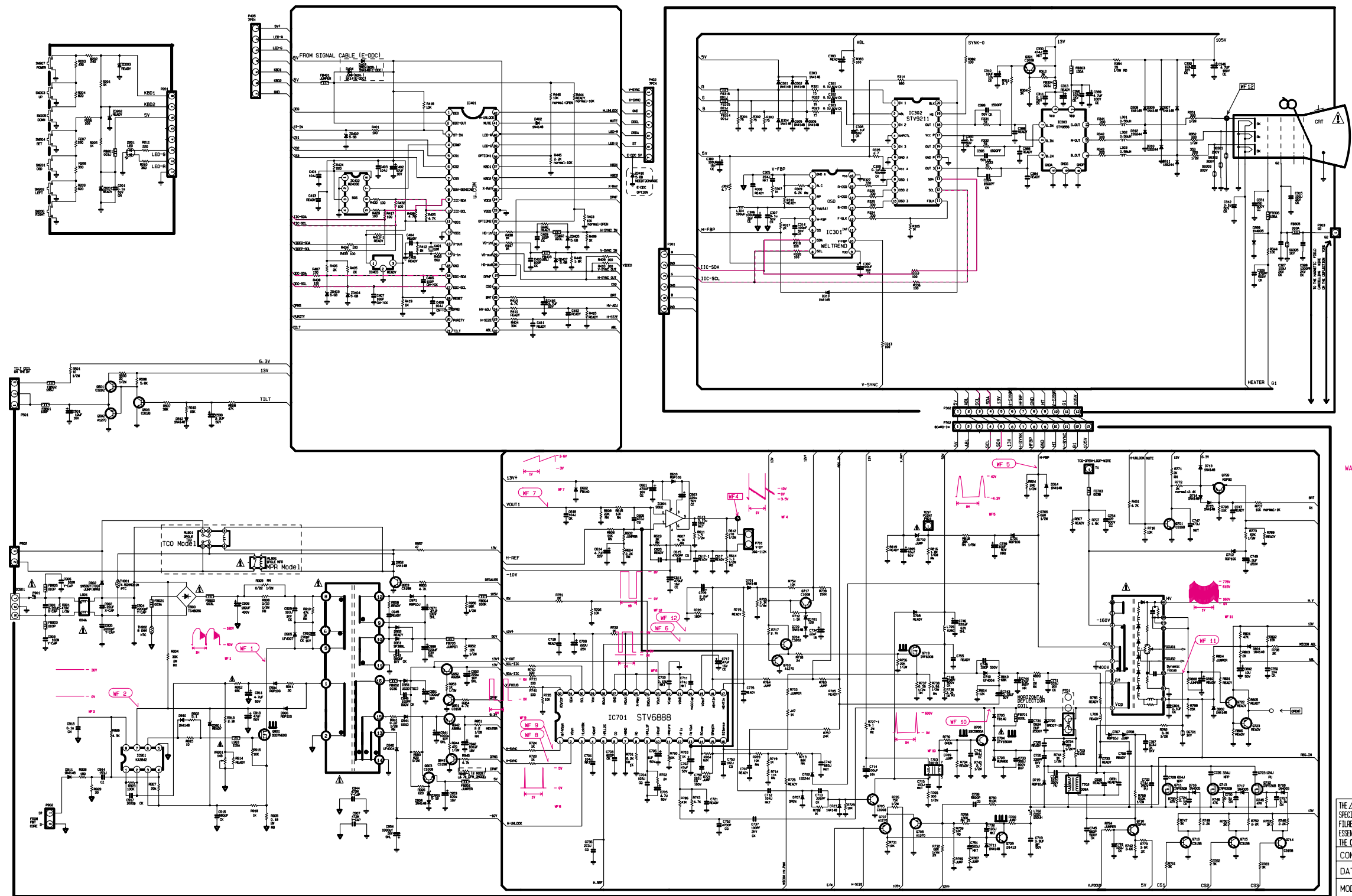


BLOCK DIAGRAM



SCHEMATIC DIAGRAM

----- DDC-SDA ----- IIC-SDA
----- DDC-SCL ----- IIC-SCL



WAVE FORM CONDITION 1: 110V
2: 60kHz / 75Hz

THE SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILTRATION AND ELECTRICAL SHOCK HAZARDS. WHEN SERVICING IT IS ESSENTIAL THAT ONLY MANUFACTURER SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMATIC.

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